

06-09-03

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
08228/019001

In Re Application Of: Shiro SAKAI et al.

Serial No.
10/044,686

Filing Date
01/11/2002

Examiner

Group Art Unit
1765

Title: NITRIDE SEMICONDUCTOR CHIP AND METHOD FOR MANUFACTURING NITRIDE
SEMICONDUCTOR CHIP

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Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last.

37 CFR 1.97(c)

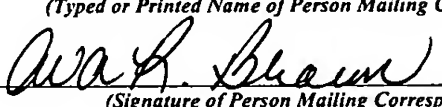
2. ☐ The Information Disclosure Statement submitted herewith is being filed after three months of the filing of a national application, or the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or after the mailing date of a first Office Action on the merits, whichever occurred last but before the mailing date of either:
1. a Final Action under 37 CFR 1.113, or
 2. a Notice of Allowance under 37 CFR 1.311,
- whichever occurs first.

Also submitted herewith is:

- ☐ a certification as specified in 37 CFR 1.97(e);

OR

- ☐ the fee set forth in 37 CFR 1.17(p) for submission of an Information Disclosure Statement under 37 CFR 1.97(c).

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)			Docket No. 08228/019001
Applicant(s): Shiro SAKAI et al.			
Serial No. 10/044,686	Filing Date 01/11/2002	Examiner	Group Art Unit 1765
Invention: NITRIDE SEMICONDUCTOR CHIP AND METHOD FOR MANUFACTURING NITRIDE SEMICONDUCTOR CHIP			
<p>I hereby certify that the following correspondence:</p> <div style="border: 1px solid black; padding: 10px; text-align: center;">Information Disclosure Statement</div> <p style="text-align: center;"><i>(Identify type of correspondence)</i></p> <p>is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: The Assistant Commissioner for Patents, Washington, D.C. 20231 on</p> <p style="text-align: center;"><u>June 6, 2003</u> <i>(Date)</i></p> <div style="text-align: right; margin-top: 20px;"><p><u>Ava R. Brown</u> <i>(Typed or Printed Name of Person Mailing Correspondence)</i></p><p><u></u> <i>(Signature of Person Mailing Correspondence)</i></p><p><u>EV322006961US</u> <i>("Express Mail" Mailing Label Number)</i></p></div>			

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SEMICONDUCTOR CHIP

COPY

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 50-0591 as described below. A duplicate copy of this sheet is enclosed.
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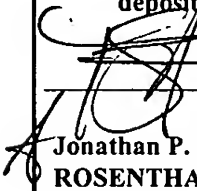
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Dated: 6/5/03

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